



# Ali Azami

Embedded Systems Engineer | Embedded AI • DSP • FPGA

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**Email** Azamiali2706@gmail.com

**Mobility** national and international

**Portfolio** ali123az.github.io/portfolio

**LinkedIn** ali-azami-27b8681b4

**Availability** immediate

## SKILLS

### Programming:

C/C++, Python, Rust

### Embedded Systems:

RTOS, ARM, STM32, Embedded AI (MCU/NPU/DSP), signal processing, CAN, SPI, I2C, UART

### Tools & Environment:

Git, GitLab, Docker, Linux, CMake, VSCode, Keil, MCUXpresso, CI/CD, MATLAB, Simulink, Embedded Linux

### Software & AI:

PyTorch, TensorFlow, TensorFlow Lite, computer vision (OpenCV)

### Digital Design:

VHDL, Verilog, FPGA, SoC, DSP

## SOFT SKILLS

- Analytical and critical thinking
- Teamwork and communication
- Autonomy and initiative
- Adaptability and fast learning

## VOLUNTEERING

**Erasmus Student Network**  
**Toulouse**

Active member

**EHTP Electro-Power**

Vice President

**Enactus EHTP**

Team Leader

## LANGUAGES

French (bilingual)  
English (advanced)  
Spanish (intermediate)  
Japanese (A2)

## REFERENCES

**Maxime Rateau** – Schaeffler France  
Available on LinkedIn

## Profile

With over one and a half years of experience through apprenticeship and internships in electronics and **artificial intelligence** for **embedded systems**, along with a strong background in **signal processing**, I am seeking a new opportunity to continue my professional career. Curious and rigorous, I am passionate about technological innovation and motivated by ambitious projects combining technical depth and creativity.

## Professional Experience

### Embedded AI Radar Systems/Software Engineer (Apprenticeship – M2)

*Vitesco Technologies / Schaeffler France, Toulouse – France* September 2024 – October 2025

- Design of a complete embedded pipeline: UWB radar acquisition, FFT (DSP), real-time classification via CNN.
- Development of signal processing algorithms optimised for microcontrollers (real-time, memory constraints).
- Integration and optimisation of embedded deep learning models (latency, RAM/Flash).
- Field testing and validation of the solution.
- Technical documentation and collaboration with international teams.

### Embedded AI Systems/Software Engineer (Internship – M1)

*NXP Semiconductors, Toulouse – France* May 2024 – September 2024

- Implementation of an Edge AI pipeline: sensor acquisition, model training, deployment on MCU/NPU.
- Optimisation of neural network inference for constrained embedded systems.
- Performance analysis (latency, memory) and adaptation to hardware constraints.
- Authoring of comprehensive technical documentation for educational use.

### Electronics & IoT Engineer (Research Internship)

*EHTP, Casablanca – Morocco* April 2022 – October 2022

- Design and implementation of a distributed embedded architecture (Arduino Mega and Raspberry Pi 4) for a tracked mobile robot with a 6-axis arm.
- Development of a computer vision system using YOLOv5 for object detection.
- Implementation of a connected autonomous charging system with MQTT-based communication and monitoring.

## Education

### Engineering Degree – Electronics, Electrical Energy and Automation

*ENSEEIH, Toulouse – France* September 2023 – September 2025

Specialisation in electronics and AI for embedded systems.

Double degree in collaboration with my home institution in Morocco.

### Engineering Degree – Electrical & Electronic Engineering

*EHTP, Casablanca – Morocco* September 2021 – September 2025

### Preparatory Classes (MPSI/MP)

*IBN Ghazi Preparatory Classes, Rabat – Morocco* September 2018 – June 2020